REMARKS

Claims 11-27, 32, and 41-64 are canceled and new claims 65-110 are added. The new claims are supported at least by Fig. 2 and the associated specification teachings at pages 7-14 of the originally-filed application.

The new claims are believed to be allowable over the previously asserted references of record including U.S. Patent No. 5,872,019 to Lee et al., U.S. Patent No. 6,326,221 to Lee et al., U.S. Patent No. 5,808,983 to Benjamin et al. and U.S. Patent No. 5,655,940 to Hodson et al. The Lee patents merely provide emitter configurations and fail to disclose or suggest *plural emitter regions in combination with the monolithic semiconductive substrate* limitations as claimed. Benjamin is related to liquid crystal display technology and is entirely irrelevant to the emitter teachings of the Lee patents or Hodson, or the addressing of the emitters of the Lee patents or Hodson. Finally, the Hodson patent is clearly related to the formation of a display using a plurality of discrete plates which are placed against one another and mounted to a glass substrate (col. 5, lines 43-54). At col. 6, lines 28-44 of Hodson, it is disclosed that Hodson specifically relies upon the use of multiple emitter plates and accordingly fails to teach or suggest limitations of the pending claims.

Applicant respectfully requests allowance of all pending claims.

The Examiner is requested to phone the undersigned if the Examiner believes such would facilitate prosecution of the present application. The undersigned is available for telephone consultation at any time during normal business hours (Pacific Time Zone).

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Appl. No. 09/251,172 Amdt. dated 5/10/2004 Reply to Office Action dated 3/8/2004

Respectfully submitted,

Dated:

By:

James D. Shaurette Reg. No. 39,833